IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Prior Application: Y. ITOU et al
 USSN 09/266,634
 Filed: March 11, 1999

Group Art Unit: 2122
Examiner: C. Das
For: METHOD OF REDUCING UNNECESSARY
 BARRIER INSTRUCTIONS

INFORMATION DISCLOSURE STATEMENT

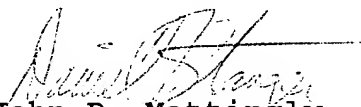Assistant Commissioner of Patents
Washington, D.C. 20231

Sir:

In accordance with the duty of disclosure, Applicants inform the Examiner of the documents cited during prosecution of the parent application, USSN 09/266,634.

The above-referenced patent application is a continuation application of U.S. Serial No. 09/266,634, filed March 11, 1999, from which priority is claimed under 35 U.S.C. § 120.

Applicants request the Examiner to initial and return a copy of the attached PTO-1449 form to indicate that the references have been considered.

Respectfully submitted,

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Registration No. 30,293
Attorney for Applicants

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Date: August 9, 2001

FORM PTO-1449 (REV. 7-80)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. H-721-02		SERIAL NO.
LIST OF DOCUMENTS CITED BY APPLICANT *(Use several sheets if necessary)*		APPLICANT Y. ITOU et al		
		FILING DATE 8/9/01		GROUP 2122

U.S. PATENT DOCUMENTS

* EXAMINER INITIAL		DOCUMENT	DATE	NAME	CLASS	SUBCLASS	FILING DATE (If Appropriate)
e·D	AA	6,216,174	4/01	Scott et al			
C·D	AB	6,098,089	8/00	O'Connor et al			
C·D	AC	5,953,736	9/99	O'Connor et al			
e·D	AD	5,873,105	2/99	Tremblay et al			
e·D	AE	5,781,775	7/98	Ueno			
C·D	AF	5,778,423	7/98	Sites et al			
C·D	AG	6,016,505	1/00	Badovinatz et al			
C·D	AH	5,802,374	9/98	Gupta et al			
C·D	AI	5,535,393	7/96	Reeve et al			
C·D	AJ	6,081,665	6/00	Nilsen et al			
	AK						

FOREIGN PATENT DOCUMENTS

		DOCUMENT	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES	NO
	AL						☐	☐
	AM						☐	☐
	AN						☐	☐
	AO						☐	☐
	AP						☐	☐

OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Pages, etc.)*

C·D	AR	Eliminating redundant barrier synchronizations in rule-based programs, Anurag Acharya, ACM, 1996.
C·D	AS	A simulation of dynamic task allocation in a distributed computer system, Ed Andert, ACM, 1987.
C·D	AT	Semantics of barriers in a non-strict, implicity-parallel language, Aditya et al, ACM, 1995.

EXAMINER Chamli C D	DATE CONSIDERED 4/16/04

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

FORM PTO-1449 (REV. 7-80)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. H-721-02	SERIAL NO.
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	AI						
	AJ						
	AK						

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	AL						☐	☐
	AM						☐	☐
	AN						☐	☐
	AO						☐	☐
	AP						☐	☐

OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Pages, etc.)*

C·D	AR	Compiler optimizations for eliminating barrier synchronization, Chau-Wen Tseng; ACM, 1995.
C·D	AS	Distributed shared memory system with improved barrier synchronization and data transfer, Tzeng, ACM, 1997.
	AT	

EXAMINER Chamli C. Da	DATE CONSIDERED 4/14/04

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.